

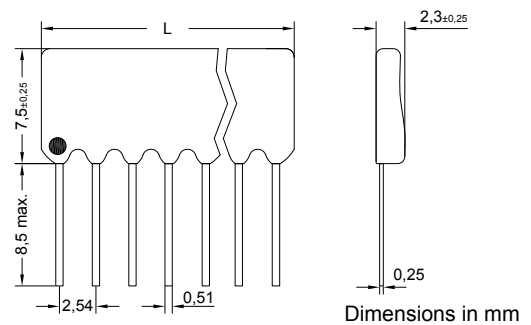
### MixNet

- 3 - 16 Pin Single In-Line Network
- Extremely wide resistance range
- Custom-built solutions
- Additional components possible

### MixPac

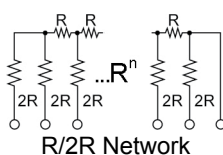
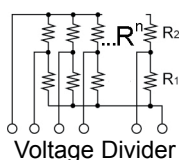
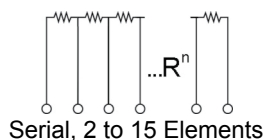
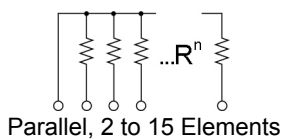
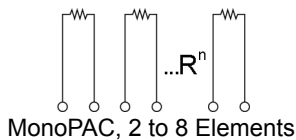
- 4 - 16 Pin SIP isolated resistors
- Space-saving assemblies
- Up to 8 individual resistive elements
- Power dissipation up to 0.25 Watts (max.)

Mechanical Data	
Material	NiCr/ RuO <sub>2</sub>
Substrate Material	Alumina
Body	Epoxy - coated
Terminals	Copper
Plating	Tin
Storage Temperature Range	-20°C to +125°C



Number of Pins	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Length L in mm (±0,5)	7,6	10,2	12,7	15,0	17,7	20,3	22,8	25,4	27,9	30,4	33,0	35,5	38,1	40,6

### Standard Circuits



### Specification

Standard Resistance Range	1Ω - 500MΩ		
Temperature Coefficient	Tracking	from 5ppm (depends on values)	
Temperature Coefficient	Absolute	down to ±5ppm to 250ppm	
Resistance Tolerance	Ratio	down to 0,1% (depends on values)	
Resistance Tolerance	Absolute	±0,1% to 30%	
Operating Voltage (max.)	100V		
Power Dissipation (max.)	0,25 Watts per element		
Operating Temperature Range	0 - 70°C		
Insulation Resistance	10.000 MΩ		

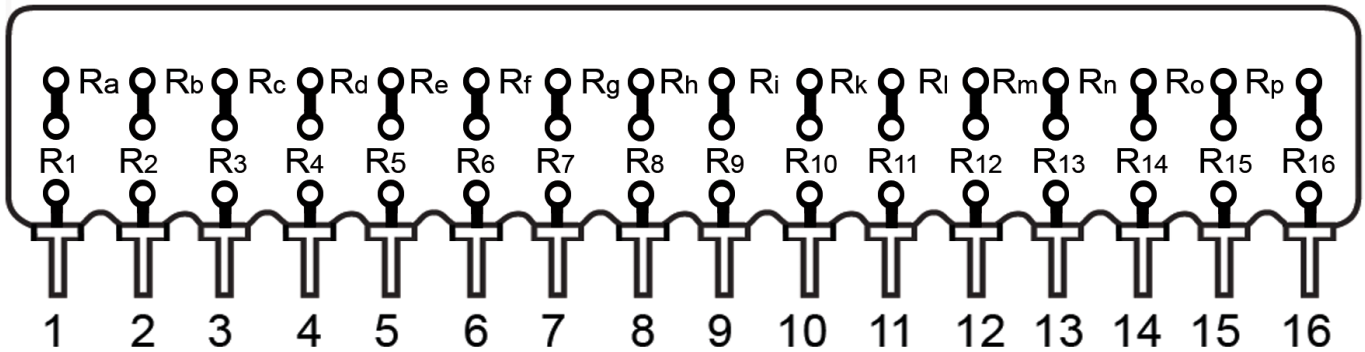
**MEGATRON MixNet und MixPac** allows the engineer to design a network with a wider range of values instead of limiting them between 100Ω-100kΩ without compromising on the precision. Thus a MixNet or MixPac is created by adding a few chip resistors which are beyond the range of 100Ω-100kΩ to the monolithic networks. It is advisable to restrict these to a small number so that the network is economical. If you need to use more of these resistors then we offer you our ChipNets. The engineer has the flexibility to also add any other components like chip fuse, diode, coils, capacitor to the MixNet network.

Please use for your inquiries and application our form or ask our well trained technical staff!

# Precision Resistors

Resistor Network

Series MIX

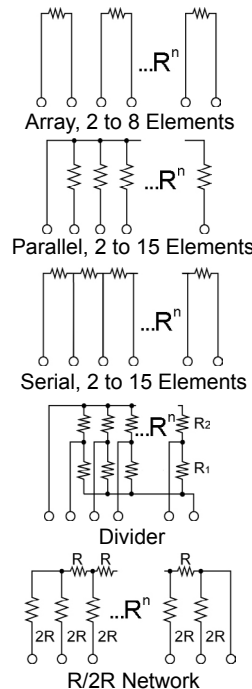


## Configuration

- Choice of the typical circuit
- Definition of the number of necessary connections
- Predefinition of the tolerance, absolutely and matching tolerance („-Pac“ resistor elements can be paired if requested to measured sets for a tolerance and TCR performance)
- Definition of the TCR, absolute and tracking
- Definition of the separate resistance values (with „-PAC“ and „chip“ variants different tolerances and temperature coefficients between single resistors are realizable)

Circuit	Number of Pins	TCR absolute	TCR tracking	Tolerance absolute	Tolerance Ratio

	Function o--o/o o	Value ( $\Omega$ )	Remark (e.g. 5ppm, 0,01%)
R <sub>1</sub>			
R <sub>2</sub>			
R <sub>3</sub>			
R <sub>4</sub>			
R <sub>5</sub>			
R <sub>6</sub>			
R <sub>7</sub>			
R <sub>8</sub>			
R <sub>9</sub>			
R <sub>10</sub>			
R <sub>11</sub>			
R <sub>12</sub>			
R <sub>13</sub>			
R <sub>14</sub>			
R <sub>15</sub>			
R <sub>16</sub>			



	Function o--o/o o	Value ( $\Omega$ )	Remark (e.g. 5ppm, 0,01%)
R <sub>a</sub>			
R <sub>b</sub>			
R <sub>c</sub>			
R <sub>d</sub>			
R <sub>e</sub>			
R <sub>f</sub>			
R <sub>g</sub>			
R <sub>h</sub>			
R <sub>i</sub>			
R <sub>k</sub>			
R <sub>l</sub>			
R <sub>m</sub>			
R <sub>n</sub>			
R <sub>o</sub>			
R <sub>p</sub>			

R<sub>1</sub>...R<sub>16</sub>, R<sub>a</sub>...R<sub>p</sub> = Position  
o--o = closed by element or contact  
o o = not connected

Example for an array: R<sub>1</sub>= o--o; R<sub>a</sub>=10k $\Omega$ ; R<sub>2</sub>= o--o; R<sub>b</sub>= o o;...  
Example Parallel: R<sub>1</sub>= o--o; R<sub>a</sub>=o--o; R<sub>2</sub>= 10k $\Omega$ ; R<sub>b</sub>= o--o;...  
Divider: R<sub>1</sub>= 1k $\Omega$ ; R<sub>a</sub>=o--o; R<sub>2</sub>= o--o; R<sub>b</sub>= o--o; R<sub>3</sub>= 1k $\Omega$ ;...

With the receipt of this configuration data sheet we immediately create for you a cost offer about prices, least order amounts or if necessary initial costs as well as delivery times.